#### Preorder-constrained simulation for program refinement with effects Koko Muroya (RIMS, Kyoto University), Takahiro Sanada (RIMS, Kyoto University), Natsuki Urabe (NII) Goal: coinductive, non-syntactical technique for proving program refinement in the presence of effects coinductive: **Program refinement** $t \sqsubseteq u$ : "stepwise" reasoning ✓ mutable state, errors "observation of evaluating t is also ✓ nondeterministic choice non-syntactical: observable in evaluating *u*" cf. applicative bisimilarity [Abramsky '90] √ I/O × probabilistic choice

### Example: reduction semantics as NA

nondeterministic automaton

$$\mathscr{A}_{\Omega} = (\mathbf{T}_{\Omega} \cup \{\checkmark\}) \{\tau\} \cup \overline{\Omega} \cup \mathbb{N} \rightarrow \{\checkmark\})$$

## Starting point: counting simulation [M. '20]

**<u>Def.</u>** Let Q be a preorder on  $\mathbb{N}$ .

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for  $\lambda$ -calculus w/ algebraic effects  $\Omega$  :

- terms  $T_{\Omega}$  as states
  - ✓ as a final state
- algebraic operations  $\overline{\Omega}$  & ground results  $\mathbb{N}$  as labels where  $\overline{\Omega} = \{f_i \mid f \in \Omega, 0 \le i \le \operatorname{arity}(f) - 1\}$
- reduction as transition

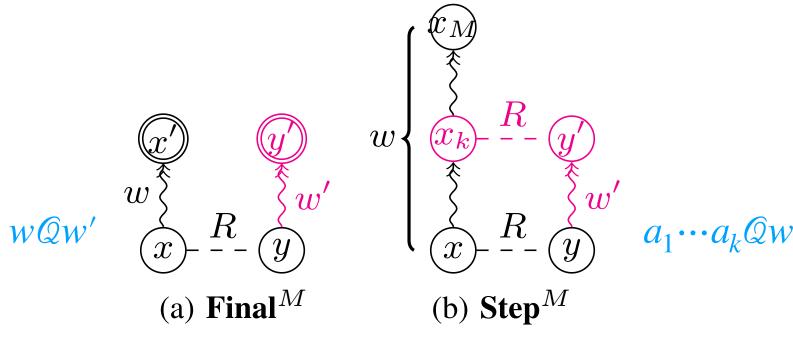
$$\frac{f \in \Omega}{E[(\lambda x \, . \, t) \, v] \xrightarrow{\tau} E[t[v/x]]} \qquad \frac{F[f(t_0, \dots, t_{\operatorname{arity}(f)-1})] \xrightarrow{f_i} E[t_i]} \qquad \underline{n} \xrightarrow{n} \checkmark$$

Program refinement as "trace inclusion"

• error 
$$t \sqsubseteq_{\{\text{err}:0\}}^{Q} u \iff$$
  
 $t \xrightarrow{w} \checkmark \checkmark \implies u \xrightarrow{w'} \checkmark \land |w|Q|w|$ 

## Proposal: Preorder-constrained simulation

**<u>Def.</u>** Let  $M \in \mathbb{N}$ , and let  $\hat{Q}$  be a preorder on  $\Sigma^*$ .



#### Thm.

For  $\mathcal{A}$   $\rightarrow$   $\exists R: (M, \tilde{Q})$ -sim  $tRu \implies t \Box^Q$ 

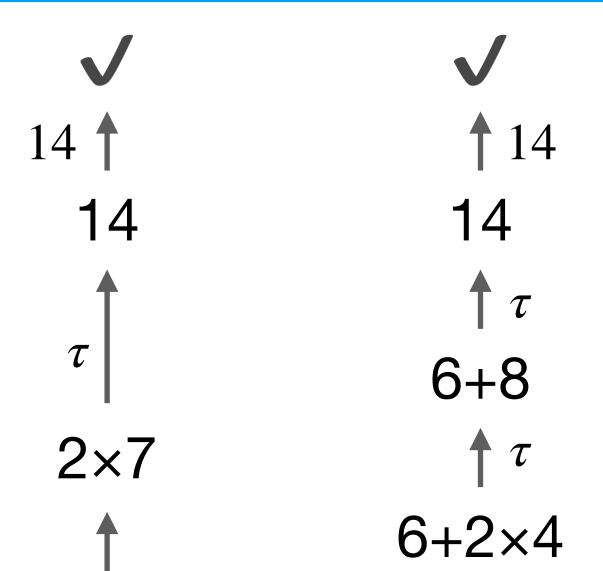
• nondeterministic choice  $t \sqsubseteq_{\{\text{or}:2\}} u \iff$ 142' 147

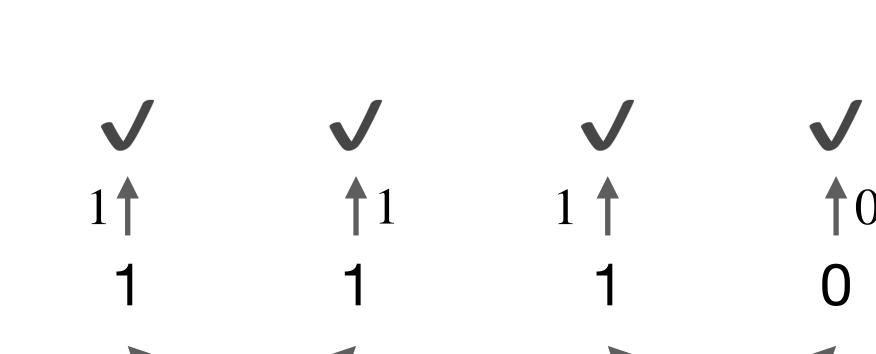
$$t \xrightarrow{w} \checkmark \implies u \xrightarrow{w} \checkmark \land w =_{\text{remove}(\tau, \text{or}_0, \text{or}_1)} w'$$

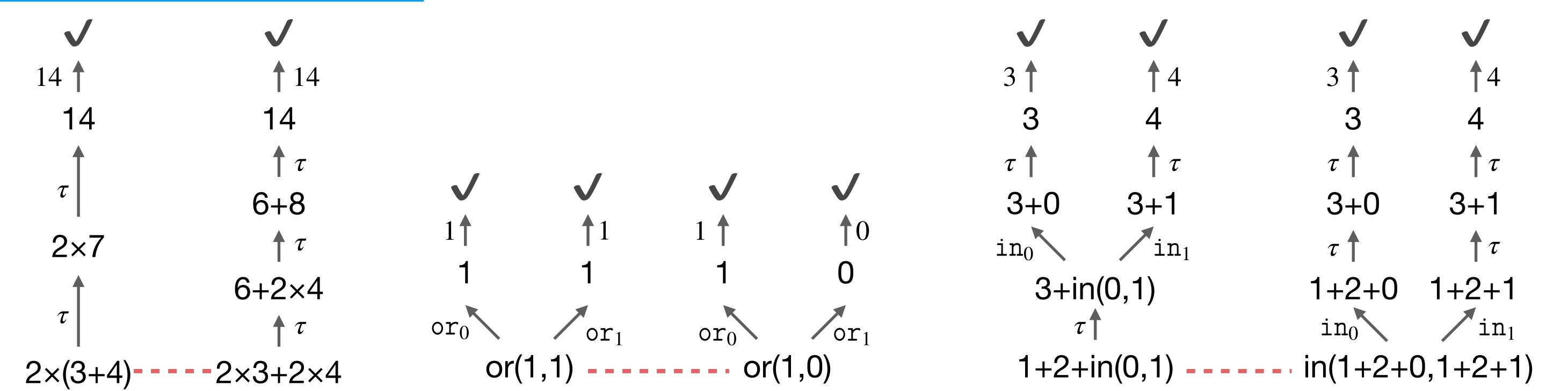
• I/O 
$$t \sqsubseteq_{\{\text{in:2,out}_0:1,\text{out}_1:1\}} u \iff$$
  
 $t \xrightarrow{w} \checkmark \checkmark \Longrightarrow u \xrightarrow{w'} \checkmark \land w =_{\text{remove}(\tau)} w'$ 

• For 
$$\mathscr{A}_{\{\text{or}\}}$$
,  $\exists R: (M, =_{\text{remove}(\tau, \text{or}_0, \text{or}_1)})\text{-sim. } tRu \implies t \sqsubseteq_{\{\text{or}\}} u$   
• For  $\mathscr{A}_{\{\text{in}, \text{out}_0, \text{out}_1\}}$ ,  
 $\exists R: (M, =_{\text{remove}(\tau)})\text{-sim. } tRu \implies t \sqsubseteq_{\{\text{in}, \text{out}_0, \text{out}_1\}} u$ 

# Example pairs of NAs

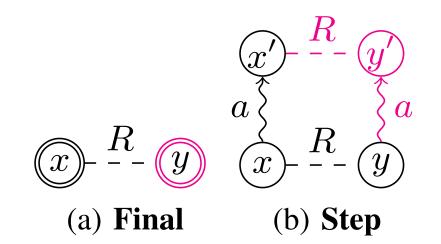






# Two challenges

Standard stepwise comparison is **not satisfactory**.



Observation varies between effects.

# Advanced topics

- generalised notion of trace inclusion
- complete variant of preorder-constrained simulation
- two-player reachability game
- up-to technique in terms of preorders